



Senior Digital IC Design and Verification Engineer

About the company

QploX is a fast-growing company offering test and automation engineering. Headquartered in Leuven, with offices in Barcelona and Eindhoven.

Our clients are major multinational enterprises and local companies from automotive, semiconductors, RF, consumer electronics.... Our Test automation group offers a one stop shop for design of automated test benches, system integration production, lab automation and data acquisition systems, with a growing focus in IoT sensor networks.

Our consultancy department offers services in RF, semiconductors and electronics design and test, as well as on the crossing roads of Nanotechnology, Bio-Science Engineering and Biotechnology.

Job Description

Our customer is a leading telecom solutions provider. Through continuous customer-centric innovation, our customer has established end-to-end advantages in Telecom Network Infrastructure, Application & Software, Professional Services and Devices. With comprehensive strengths in wireline, wireless and IP technologies, our customer has gained a leading position in the All-IP convergence age. Its products and solutions have been deployed in over 100 countries and have served 45 of the world's top 50 telecom operators, as well as one third of the world's population.

Their R&D teams develop next generation RF transceivers, supporting 4G (LTE) and 5G systems.

The Senior Digital Design & Verification Engineer has the following responsibilities:

- Integrate legacy control and data path designs in new products.
- Update and verify legacy designs to fit new product requirements.
- Design and verify new ultra-high speed DSP blocks for next gen products.
- Interface with the physical implementation team for further design and flow improvements.

This role includes technical leadership, meaning technical hands-on expertise and excellent team skills; this candidate targets high and challenging standards on technical performance, product and process quality and project schedule.

Location: Leuven

Candidate Description

- MSEE or equivalent with min 3 to 5y industrial experience
- Solid grasp of simulation concepts such as regression testing, UVM, functional coverage, assertions, ...
- Good knowledge of Verilog and SystemVerilog for design and verification
- Experience with RTL lint
- Knowledgeable about DFT and ATPG
- Knowledgeable about CDC issues and techniques for low power design
- Experience with delay annotated gatelevel simulation
- Experience with implementation of high-speed pipelined FIR DSP structures is a plus
- Formal Verification experience is a plus
- Formal lint
- Sequential Equivalence Checking

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- Assertion Based Verification
 - Experience with C/C++/SystemC models for RTL verification a must
 - Python and TCL programming experience a strong plus
 - Able to efficiently work in a Linux command line environment as well as in Windows MS Office for reporting and documentation
 - Excellent analytical skills
 - Good communication skills
 - Team-player
 - Result driven
 - Continuous strive for improvement in circuits and process
 - Detail oriented and determined
 - Willing to relocate to Leuven (Belgium) region
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We offer

An attractive salary package with extra benefits. A high tech, multicultural and young ambient. A fast track in a growing company. Formation in multidisciplinary environment plenty of learning opportunities.

Contact

Send your CV and application letter to jobs@qplox.com with the subject "**Senior Digital IC Design and Verification Engineer**".
