Analog layout engineer

About the company

Qplox is a fast growing company offering test and automation engineering. Headquartered in Leuven, with offices in Barcelona and Eindhoven, our clients are major multinational enterprises and local companies from automotive, semiconductors, RF, consumer electronics…. Our consultancy department offers services in RF, semiconductors and electronics design and test. Our Test automation group offers a one stop shop for design of automated test benches, data acquisition systems and system integration production and lab automation solutions.

Job Description

Full Custom Layout Design of various Analog blocks, taking into consideration the specifications and guidelines required for the Design, floor planning and performing layout validations.

- Full Custom Analog Layout Designer having experience of designing various Analog Blocks for various technologies.
- The candidate should be able to perform Chip Level checks after connecting various designed blocks and Top Level routing after creating IO ring.
- Block level and Top level verifications like DRC,LVS,EM/IR etc.
- Preparation of Layout Design closure reports as per requirements.

Candidate Description

- Sound knowledge about Analog Layout Fundamentals for deep submicron technologies.
- Sound knowledge about Layout Dependent effects in advanced nodes.
- Good knowledge about Virtuoso GXL will be an added advantage.

We offer

An attractive salary package with extra benefits. A high tech, multicultural and young ambient. A fast track in a growing company. Formation in multidisciplinary environment plenty of learning opportunities.

Contact

Send your CV and application letter to jobs@qplox.com with the subject " **Analog layout engineer** "